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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **DeWitt, Jr. et al.**

Serial No.: **10/675,831**

Filed: **September 30, 2003**

For: **Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations in a Data Processing System**

§ Group Art Unit: **2183**

§

§ Examiner: **Cody, Dillon J.**

§

§ Attorney Docket No.: **AUS920030479US1**

§

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§ By: Louise Fay

Louise Fay

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Alexandria, VA 22313-1450

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- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- Reference AL; and
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Respectfully submitted,

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DOCKET NUMBER AUS920030479US1



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In re application of:

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Title: **Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations in a Data Processing System**

Group Art Unit: **2183**

Intellectual Property
Law Department
International Business
Machines Corporation
11400 Burnet Road
Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

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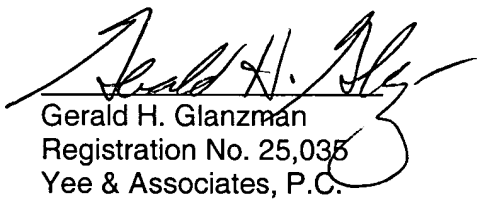
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Respectfully submitted,



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known	
Application Number	10/675,831
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Cody, Dillon J.
Art Unit	2183
Attorney Docket No.	AUS920030479US1

Examiner Initials		Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	5051944	09-24-1991	Fetterolf et al.	
	AB	5142634	08-25-1992	Fite et al.	
	AC	5394529	02-28-1995	Brown, III et al.	
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	AE	5887159	03-23-1999	Burrows	
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FOREIGN PATENT DOCUMENTS

Examiner Initial		Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Cashing, Coherence and Consistency, NY, pp. 1-14.

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